

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

ADRIAN G. SPENCER ET AL

GB 000177

Serial No.

Filed: CONCURRENTLY

METHOD AND APPARATUS FOR SYNCHRONISING FREQUENCY HOPPING
TRANSCIEVERS

Commissioner for Patents
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination,
please amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

5. A method as claimed in claim 1, further comprising
switching the second transceiver into a single-channel-at-a-
time reception mode in response to receiving the first message.

10. A radio receiver as claimed in claim 8, wherein at least
one signal received via the at least one low IF is de-rotated
to zero frequency prior to demodulation.

12. A radio receiver as claimed in claim 6, comprising control
means operable to invoke a single-channel-at-a-time reception
mode in response to receiving a predetermined message.

13. A radio receiver as claimed in claim 6, further comprising control means for selecting for further processing a signal from among a plurality of simultaneously received signals.

14. An integrated circuit comprising a receiver as claimed in claim 6.


15. A transceiver comprising a receiver as claimed in claim 6.

REMARKS

The foregoing amendments to claims 5, 10, and 12-15, were made solely to avoid filing the claims in the multiple dependent form so as to avoid the additional filing fee.

The claims were not amended in order to address issues of patentability and Applicant respectfully reserves all rights he may have under the Doctrine of Equivalents. Applicant furthermore reserves his right to reintroduce subject matter deleted herein at a later time during the prosecution of this application or continuing applications.

Respectfully submitted,

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APPENDIX

5. A method as claimed in claim 1 ~~any of claims 1 to 4~~, further comprising switching the second transceiver into a single-channel-at-a-time reception mode in response to receiving the first message.

10. A radio receiver as claimed in claim ~~8 or 9~~, wherein at least one signal received via the at least one low IF is de-rotated to zero frequency prior to demodulation.

12. A radio receiver as claimed in claim ~~6 or 7~~, comprising control means operable to invoke a single-channel-at-a-time reception mode in response to receiving a predetermined message.

13. A radio receiver as claimed in claim ~~6 or 7~~, further comprising control means for selecting for further processing a signal from among a plurality of simultaneously received signals.

14. An integrated circuit comprising a receiver as claimed in claim 6 ~~any of claims 6 to 13~~.

15. A transceiver comprising a receiver as claimed in claim 6 ~~any of claims 6 to 13~~.